



Vishleshan

Doctoral Colloquium & Research Scholars' Day~ 15-16 Feb, 2019

Jointly organized by
Department of Industrial and Systems Engineering
and IISE Student Chapter (#660)
Indian Institute of Technology Kharagpur

Sponsored by:

eugenie.ai


TATA METALIKS


INTERNITY
"TOGETHER WE CAN"



You are Cordially Invited to the Inaugural Function of



**‘Vishleshan’
Doctoral Colloquium &
Research Scholars’ Day 2019
Jointly Organized by**

**Department of Industrial and Systems Engineering
and IISE Student Chapter (#660)**

on

15-16 February, 2019

Venue: Seminar Room, ISE

Chief Patron

Prof. P. P. Chakrabarti, Director, IIT Kharagpur

Patron

Prof. Adrijit Goswami, Dean CEP, IIT Kharagpur

Chief Guest

Prof. Shailendra Singh, Director, IIM Ranchi

Guest of Honor

Mr. V. Ramesh, Vice President (Operations Excellence), Tata Hitachi

Prof. M Jenamani
RS Coordinator, ISE

Prof. S P Sarmah
Head, ISE

Dr. Jitesh J Thakkar
Prof. in Charge, IISE Student Chapter

Sponsored by:



‘Vishleshan’- Doctoral Colloquium & Research Scholars’ Day 2019

Schedule : 16th Feb, 2019

Time	Program
9.30 am – 10.00 am	Inaugural Function
10.00 am – 10.15 am	High Tea & Photo Session
10.15 am – 10.45 am	Keynote speech by Chief Guest
10.45 am – 11.15 am	Keynote speech by Guest of Honor
11.15 am – 11.30 am	Tea break
11.30 am – 1.00 pm	Poster presentations by Research Scholars
1.00 pm – 2.00 pm	Lunch
2.00 pm – 2.45 pm	Session by Prof. Soudip Roy Chowdhury (Entrepreneur in Residence, Fractal analytics)
2.45 pm – 3.30 pm	Session by Mr. Santanu Banerjee, (General Manager – Quality, Tata Metaliks Ltd.)
3.30 pm – 4.15 pm	Session by Ms. Rajshree Srivastava (Speaker, Internity Foundation)
4.15 pm – 5.30 pm	Presentations by Research Scholars
6.00 pm – 7.00 pm	Valedictory

‘Vishleshan’- Doctoral Colloquium & Research Scholars’ Day 2019

Pre-RS Day Workshop schedule : 15th Feb, 2019

Time	Program
9 am-11 am	Arena Simulation
11 am-11.15 am	Tea break
11.15 am-1.15 pm	System dynamics simulation (Vensim)
1:15 pm -2.00 pm	Working Lunch
2.00 pm-4.00 pm	Anylogic simulation
4.00 pm-4.15 pm	Tea break
4.15 pm-6.15 pm	CPLEX